**REMARKS**

Claims 1-4, 16, 17 and 27-54 are pending. Claims 1, 16, and 27 have been amended. Reconsideration is respectfully requested.

Claim 1 stands rejected under 35 U.S.C. 101 as directed to non-statutory subject matter. The Office Action states, on page 3 in the Detailed Action portion:

> "the last step outputs the second sequence of second information blocks, but that information is not tangibly stored in any location once derived from the first information block. Hence, there is no indication there the results of outputting [sic] step are tangible. Applicant's attempt to cure the deficiency by 'outputting said sequence of the second information blocks from the data processor integrated circuit to a host external to the data processor integrated circuit' still lacks tangibly storing the said second information."

This passage seems to allege that the concept of "tangible" requires storage of information as a necessary condition. Applicant is not aware of authority for such a position, and respectfully submits that the 35 U.S.C. 101 rejection is overcome by Claim 1 as presented above.
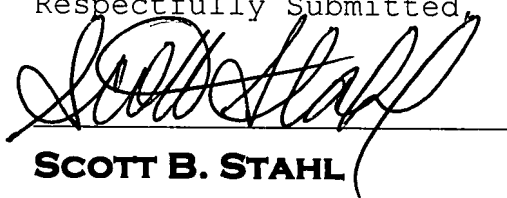
All pending rejections pursuant to 35 U.S.C. 103(a) are overcome for the following exemplary reasons.

In Claims 1-4, 16, 17 and 27-54 as presented above, each of a plurality of consecutive ones of the first information blocks contains a constituent bit of a multi-bit signal used by the data processor. In addition, each of a plurality of consecutive ones of the second information blocks contains a constituent bit of the multi-bit signal. This exemplary feature finds support, for example, in Figures 9 and 23-23B, and in the corresponding

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descriptions in the specification. This claim feature has not been found to be taught or suggested by the prior art references of record.

Applicant submits in view of the foregoing that all pending claims are allowable, and notice to that effect is respectfully solicited.

Respectfully Submitted,

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